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Class	Subclass
ISSUE CLASSIFICATION	

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PATENT NUMBER

U.S. UTILITY Patent Application

<p>O.I.P.E.</p> <p>SCANNED <u>15-3</u> Q.A. <u>Am</u></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/965509	CONT/PRIOR D	CLASS 438	SUBCLASS 234	ART UNIT 2812 2818	EXAMINER R. BERRY
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APPLICANTS

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TITLE

Methods for forming and integrated circuit structures containing enhanced-surface-area conductive layers

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ 	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)			ISSUE FEE	
				Amount Due	Date Paid
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